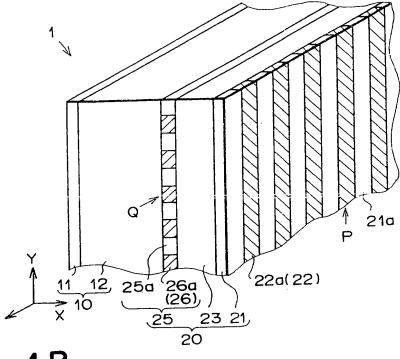
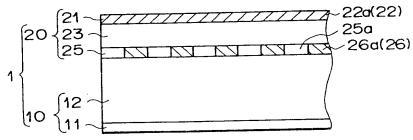
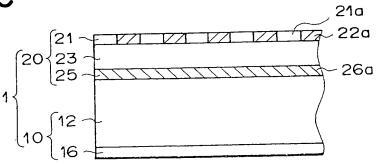
F | G.1A



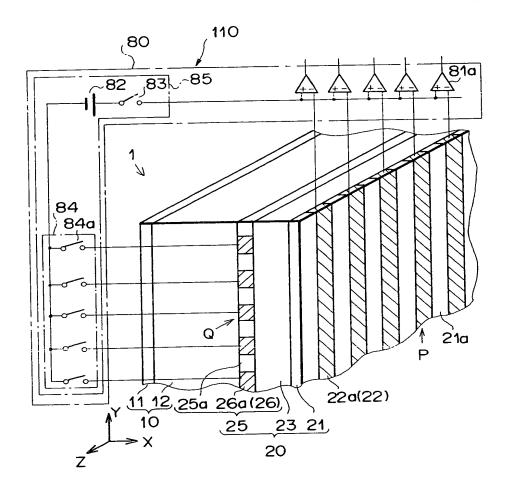
F I G.1B

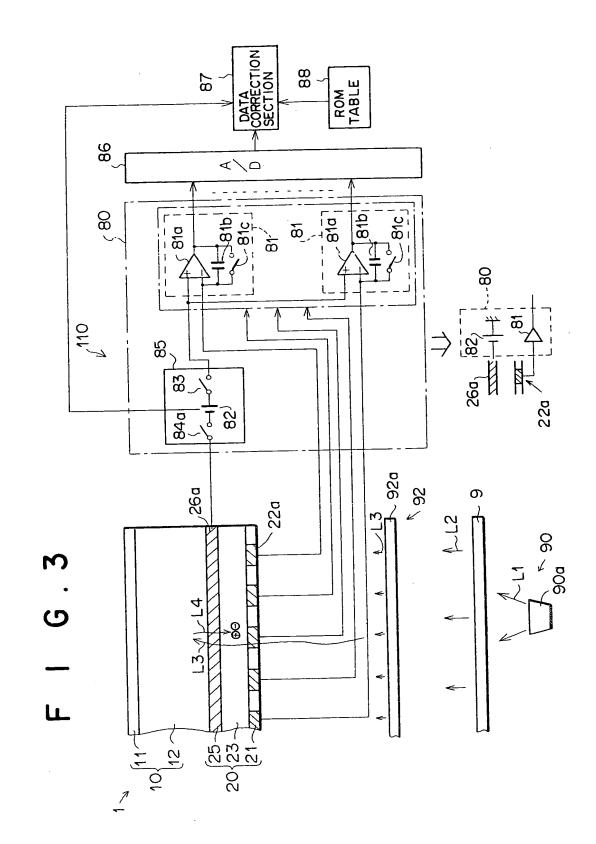


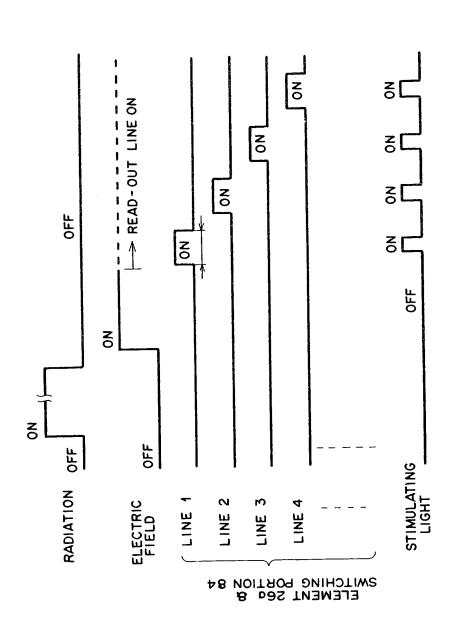
F I G.1C



F I G.2

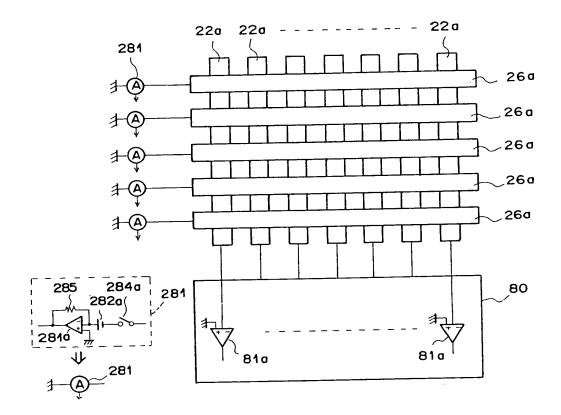




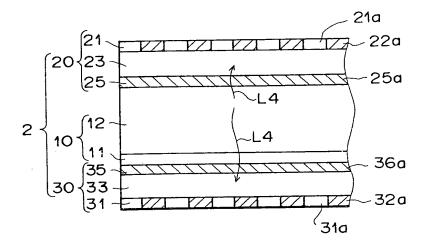


T G - T

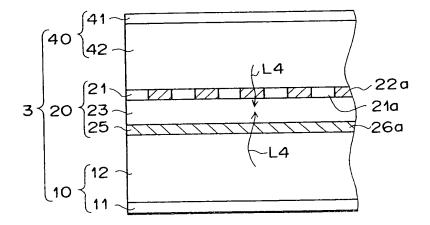
F I G.5

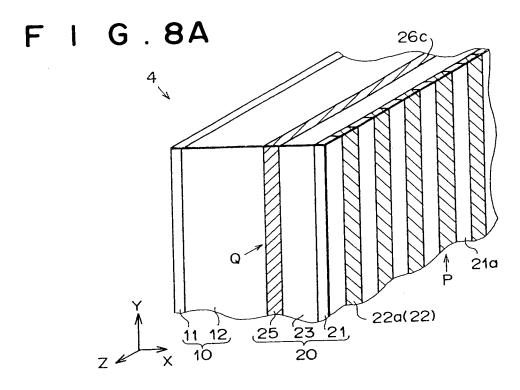


F I G.6

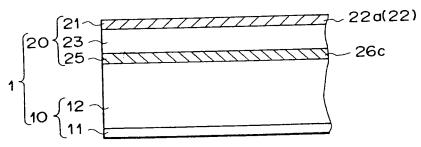


F I G.7

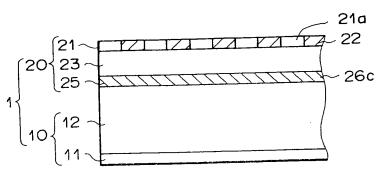


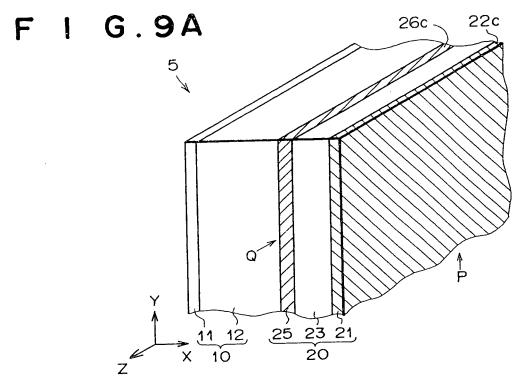


F I G.8B

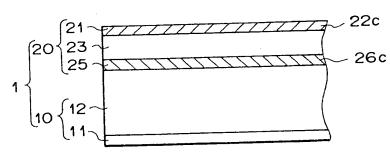


F I G.8C

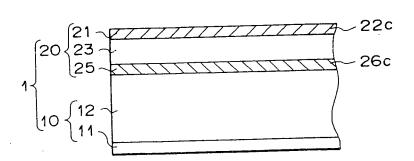


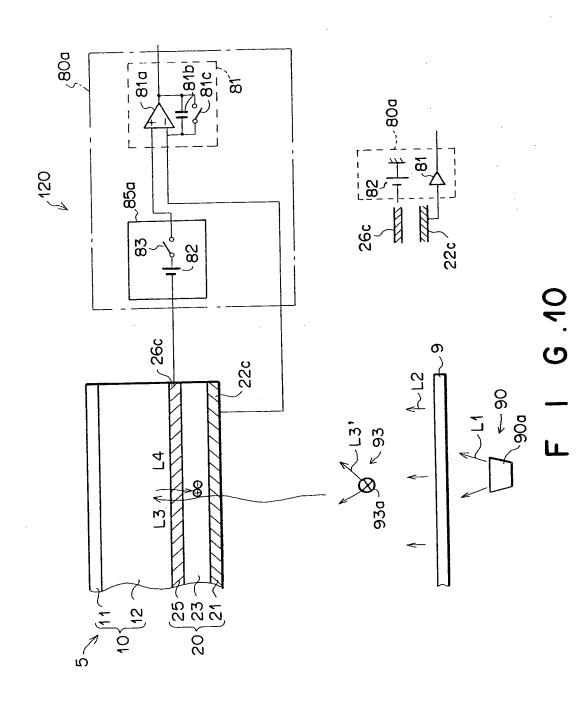


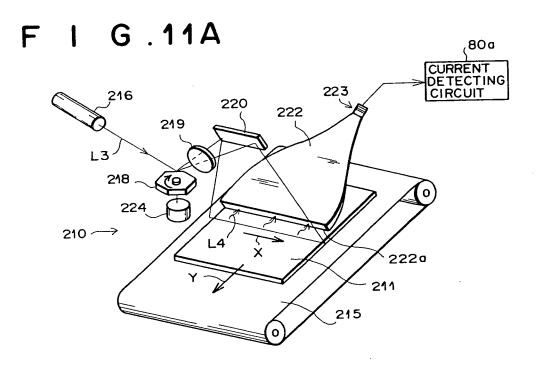
F I G. 9B



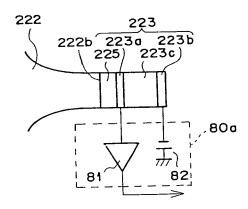
F I G.9C



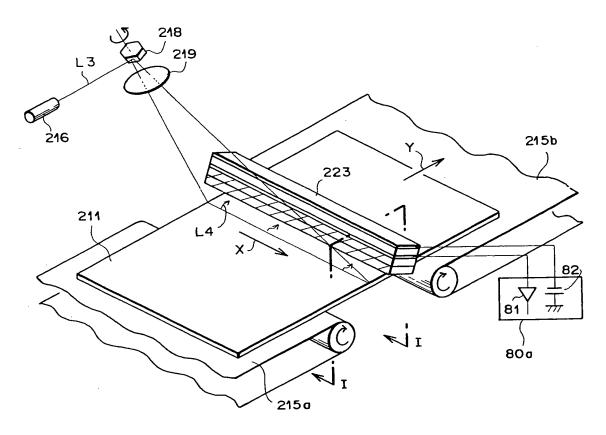




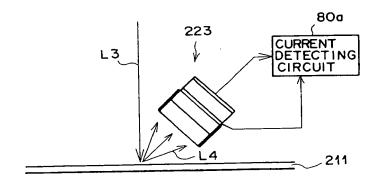
F | G.11B



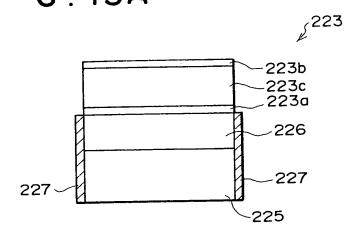
F I G . 12A



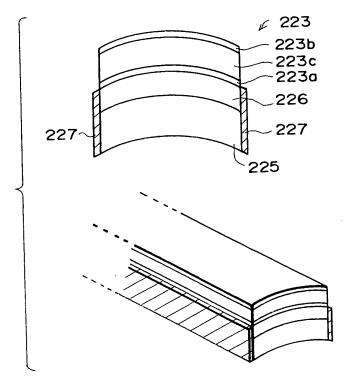
F I G.12B



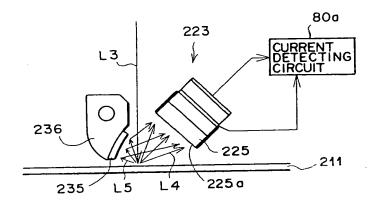
F I G.13A



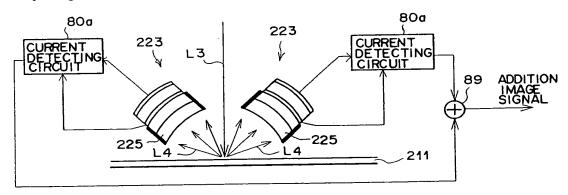
F I G.13B



F I G. 14A



F I G.14B



F I G. 14C

223

CURRENT
DETECTING
CIRCUIT

89 ADDITION
IMAGE
SIGNAL
211

225

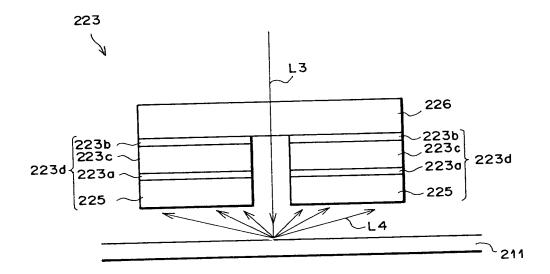
223

L6

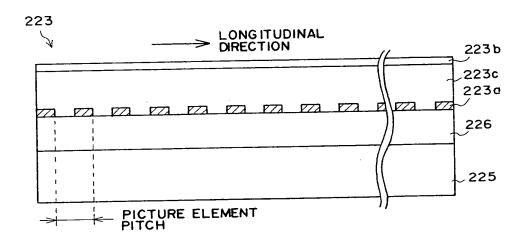
80a

CURRENT DETECTING CIRCUIT

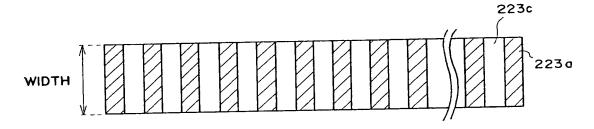
F I G.15



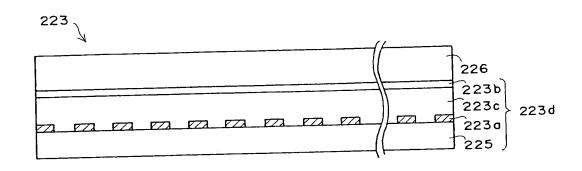
F I G.16A



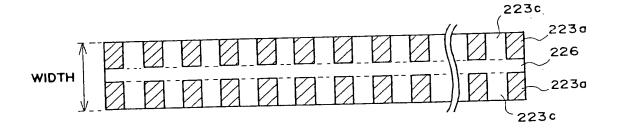
F I G.16B



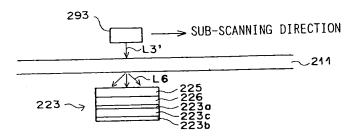
F I G.17A



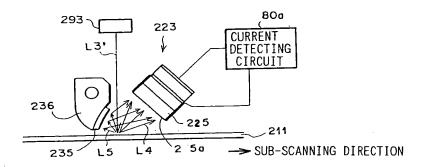
F I G.17B



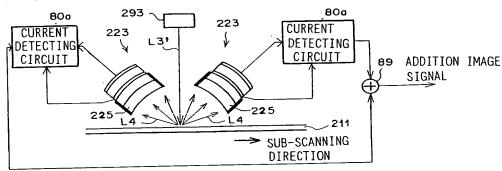
F I G.18A



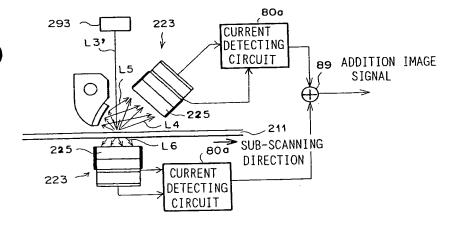
F I G.18B



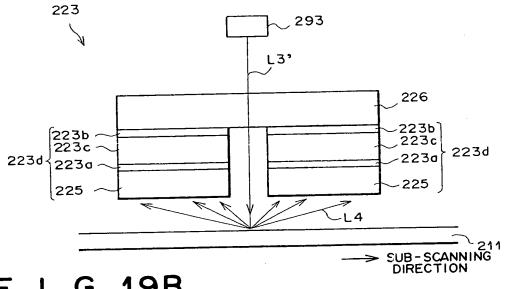
F I G.18C



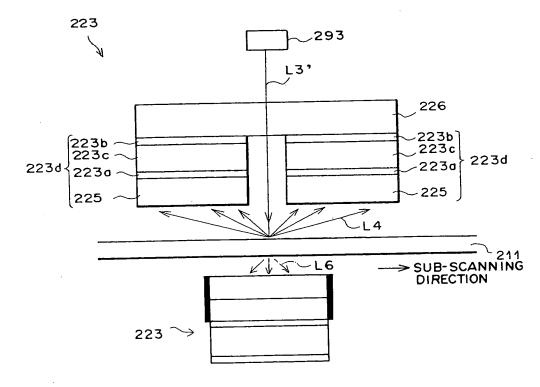
F I G . 18D



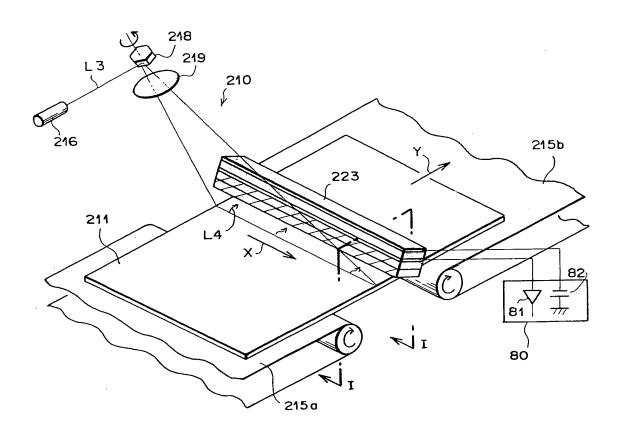
I G.19A



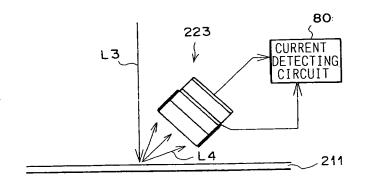
G.19B

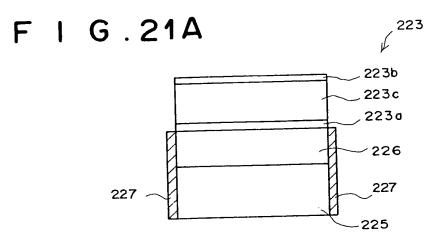


F I G. 20A

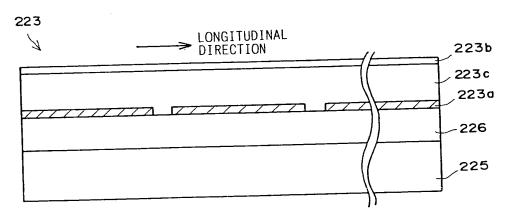


F I G. 20B

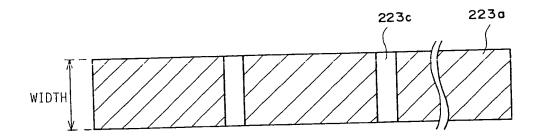


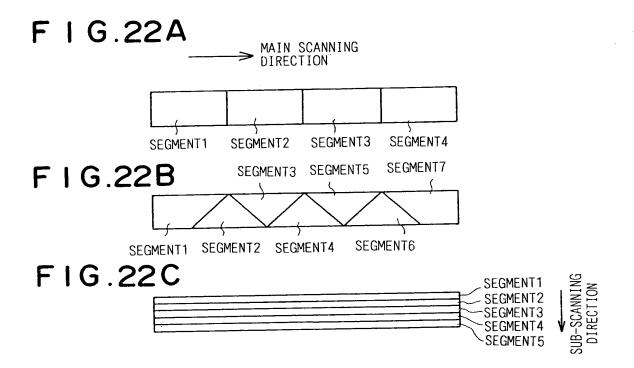


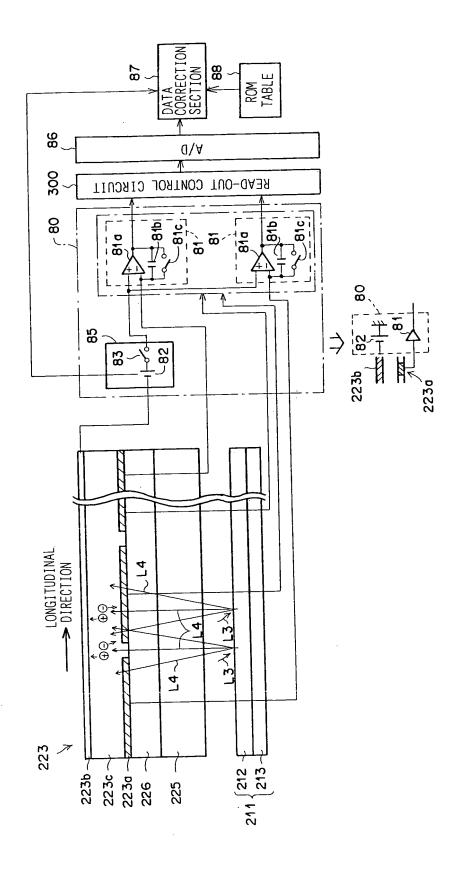
F I G. 21B



F I G.21C

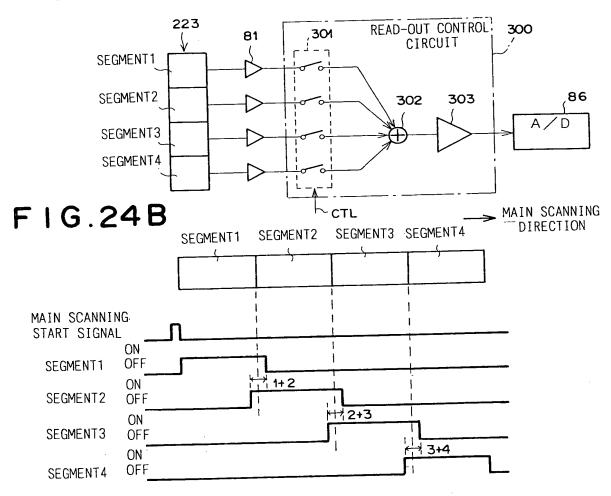


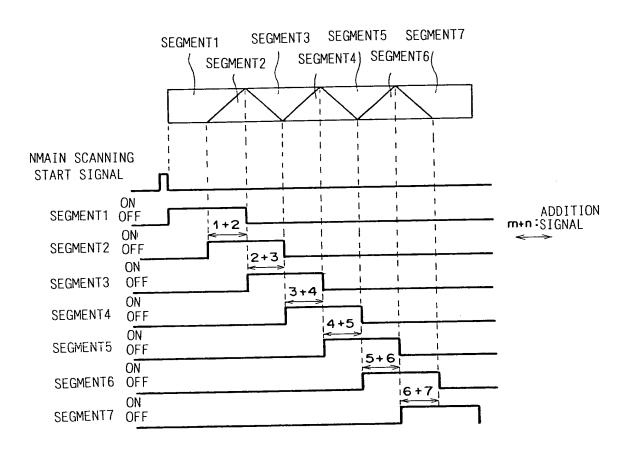




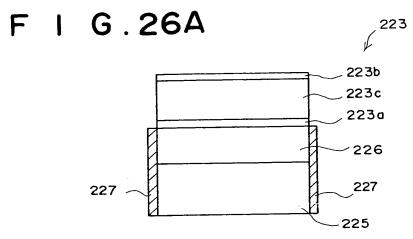
F I G.23

F I G.24A

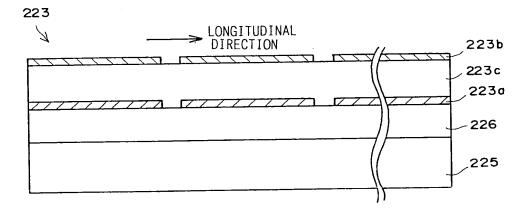




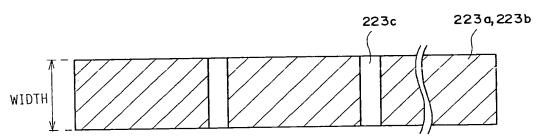
F I G.25

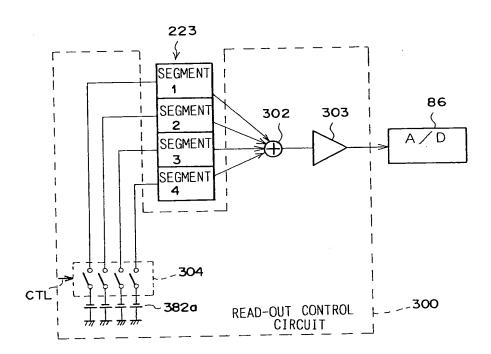


F I G.26B



F I G. 26C





F I G.27